

# Begin discussion : \$100k TPC R&D

- Focus on hardware/testing (not simulations)
  1. Read-out Electronics
  2. Gas gain / ion back flow
  3. Mechanical design (too early?)  
Useful only for ePHENIX location

# 1. Readout electronics (\$50k)

- SAMPA chips best option to limit costs
- Need to establish communication with ALICE/Sao Palo group, begin testing early designs
- Join First small batch run this October
  - Cost to order 3-4 chips for testing
  - Board design and layout
  - Begin testing
- LLNL can do this work, but for now \$50k is just a guess

## 2. Gas gain R&D

- Do we have specific (risk-mitigation) needs for gain/backflow testing ?
- Howard Wieman has new idea for fast gating-grid, but will we have a need for it ?
- Perhaps it would be more prudent to focus on electronics and have more than one group involved