WFD250 by JLAB

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- ✓ 16 LEMO inputs
- ✓ 250 Msaples/s
- ✓ 12-bit
- ✓ 200 Mbytes/s backplane readout (2eSST – 320 Mbytes/s in theory)
- ✓ 4 x 2.5 Gbit/s (3.125 Gbit/s in theory) VXS connection – trigger data output
- ✓ Possible synchronization for external clocks
- ❖ No memory onboard
- ❖ No firmware support for VXS readout
- ❖ Special modules for trigger, synchronization and clocks
- ❖ No documentation for 3d party firmware development
- ➢ VXS or VME64x crate without J0 connector required
- ➢ For a small setup “test” trigger, synchronization and clocks module supporting up to 7 WFDs can be used
- ➢ VXS readout can be done with new firmware using either special module or standard VXS switch and controller with VXS support

New HJET DAQ

- VME64x crate without J0 connector
- Single board computer – crate controller
- 6 JLAB WFD250 boards = 96 channels (on loan from JLAB)
- "test" trigger, synchronization and clocks module

  ~ 12 k$

- ✓ Complete new digitization for HJET
- ✓ No new firmware, software only
- ✓ Working system – not just a test
- ✓ Can be used for any R&D in future